

FIG. 1

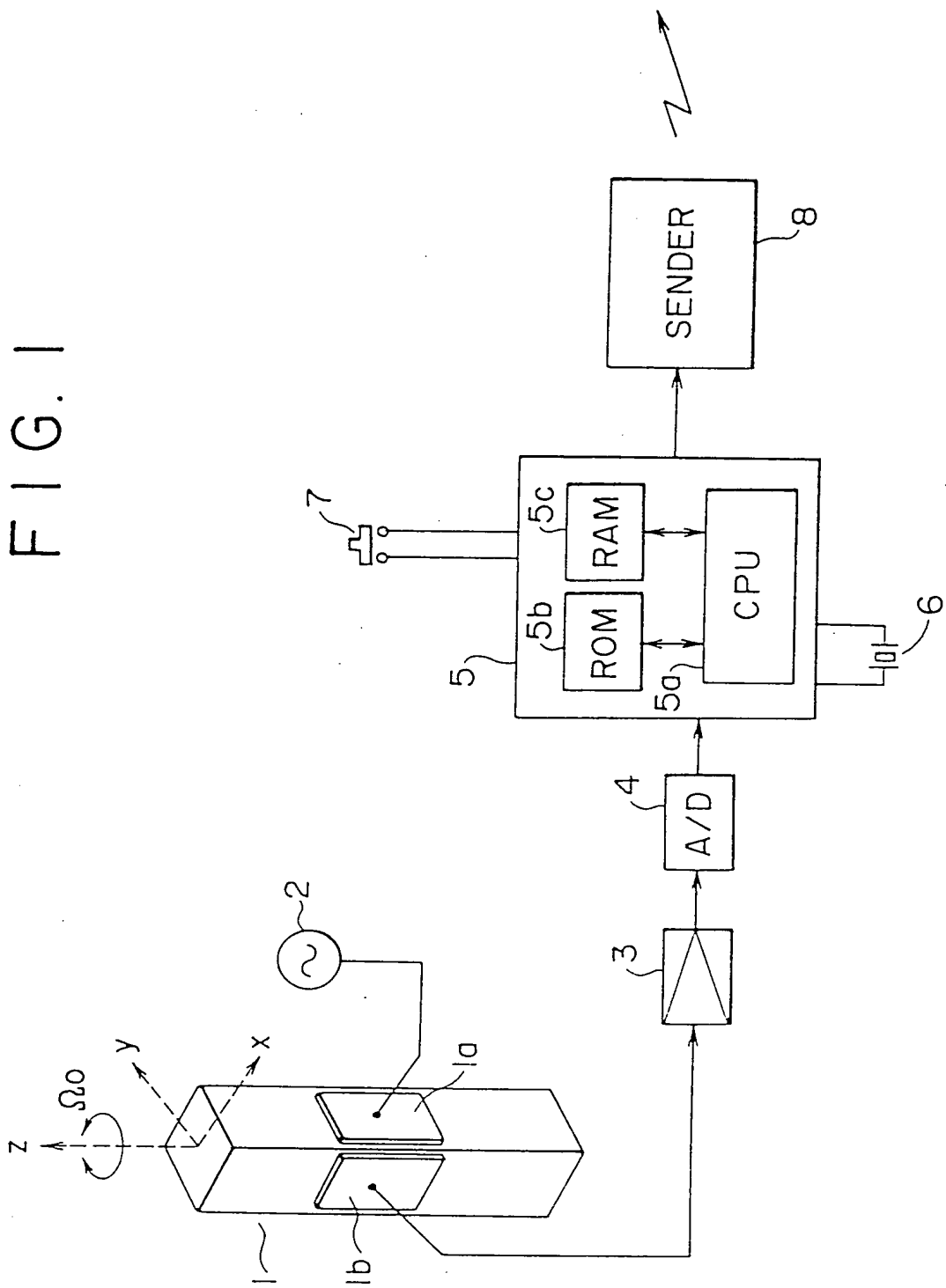


FIG. 2

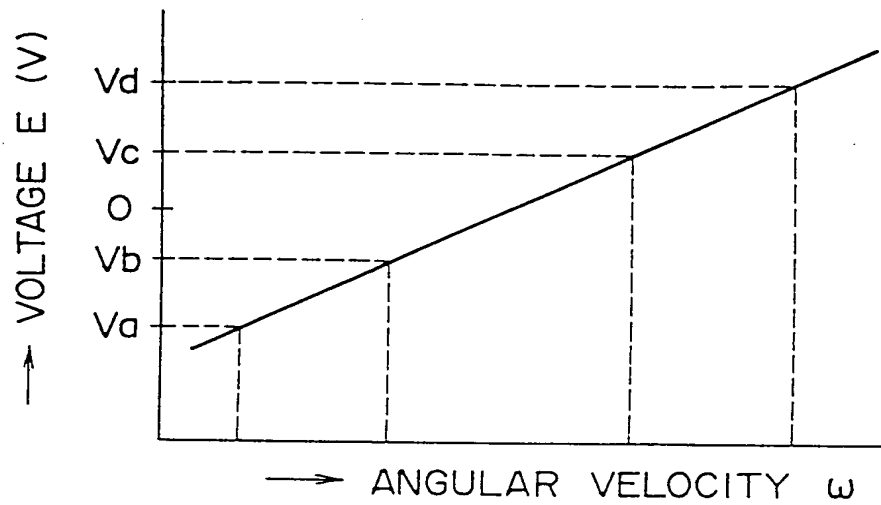


FIG. 3

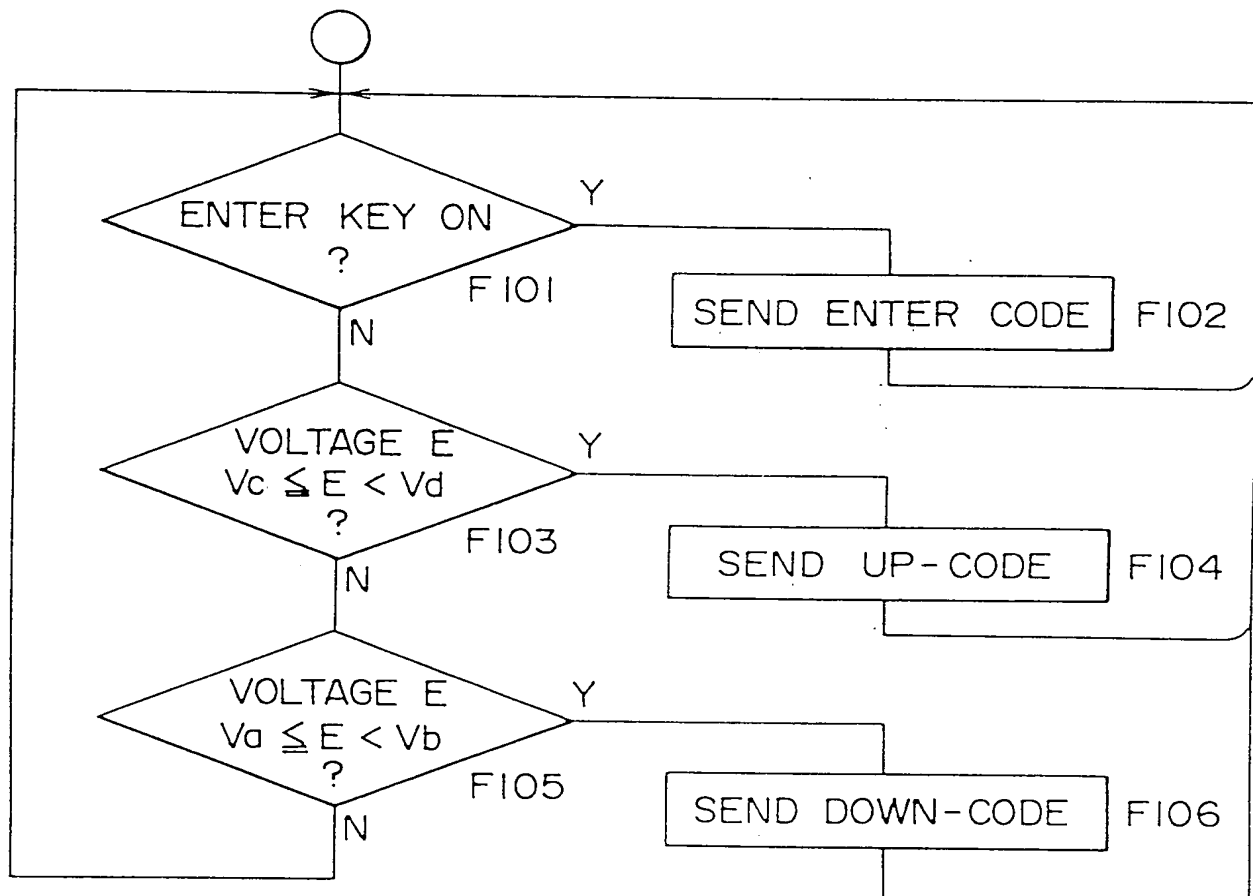


FIG. 4

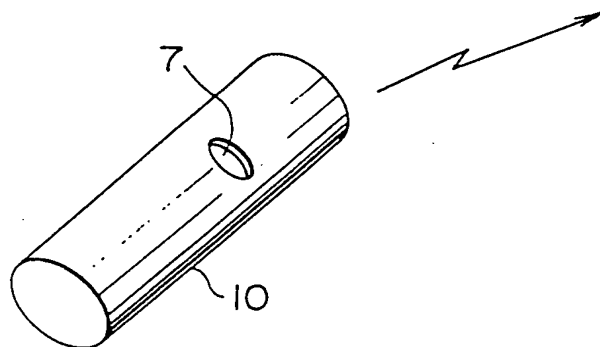


FIG. 5

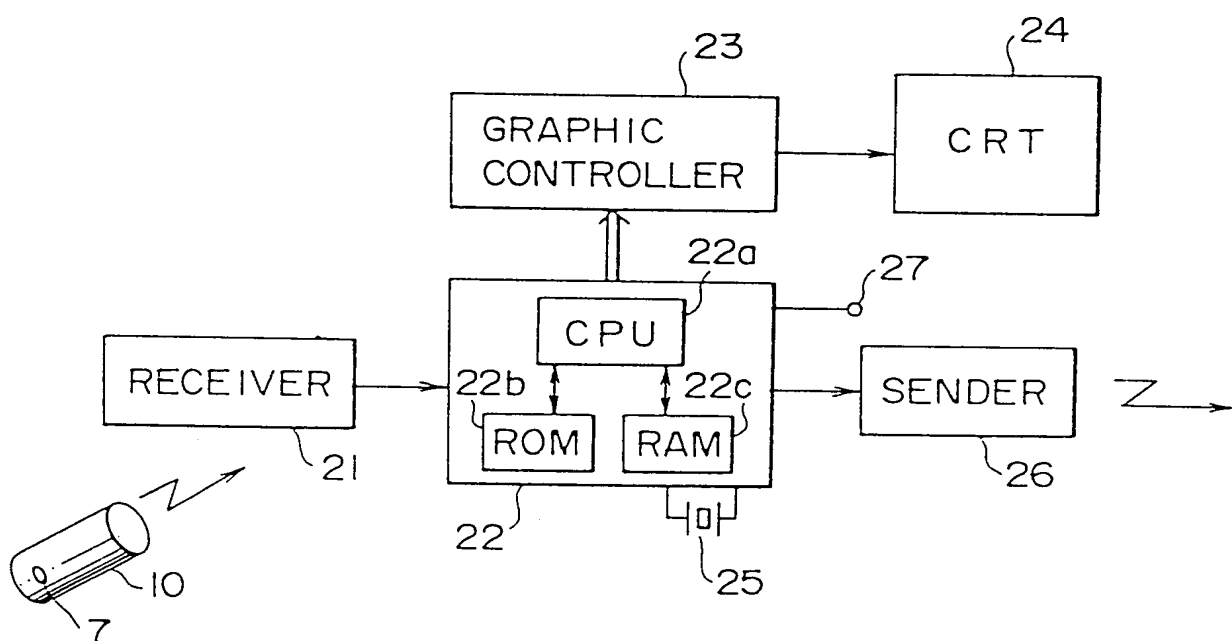


FIG. 6

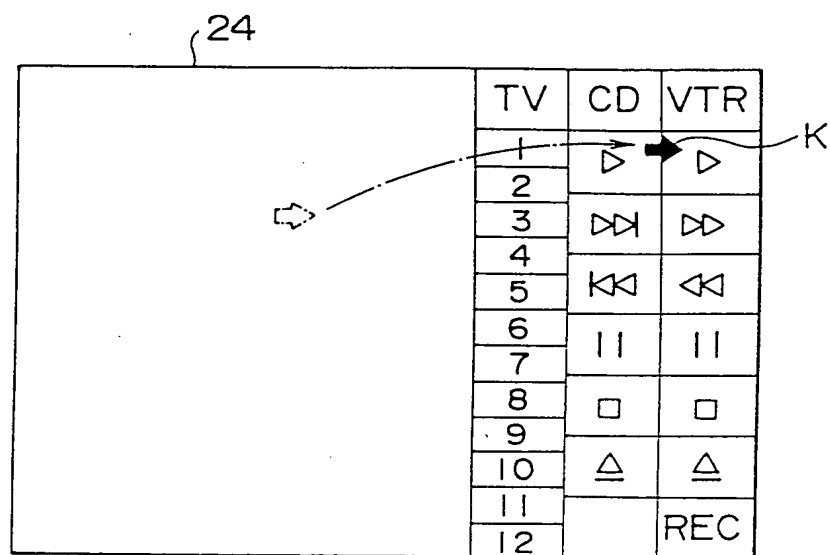


FIG. 7

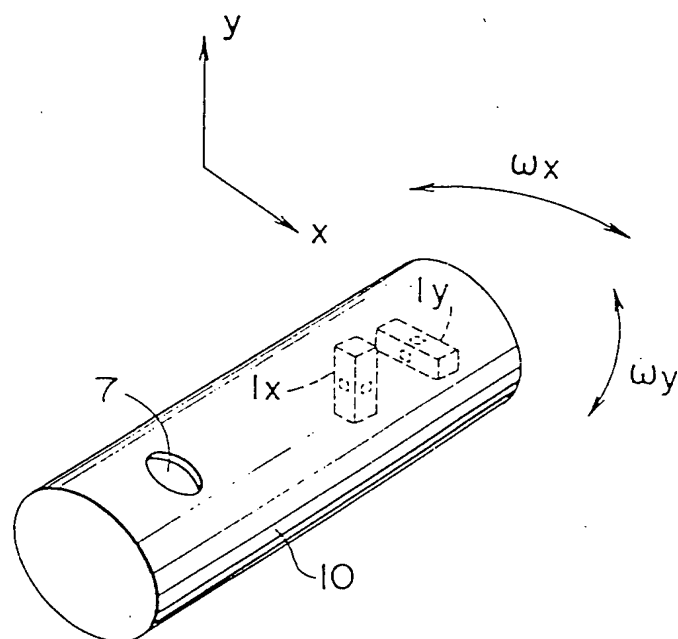


FIG. 8

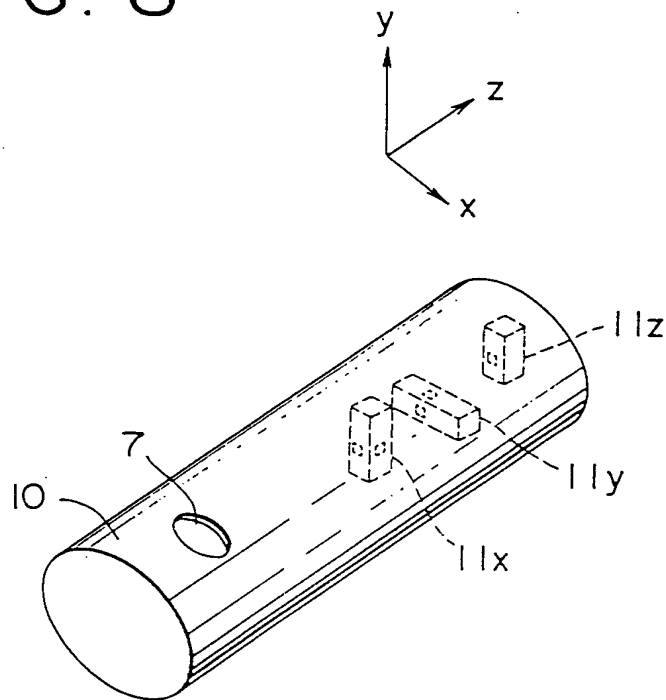


FIG. 9

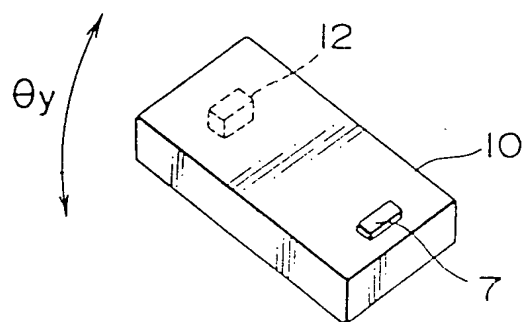


FIG. 10

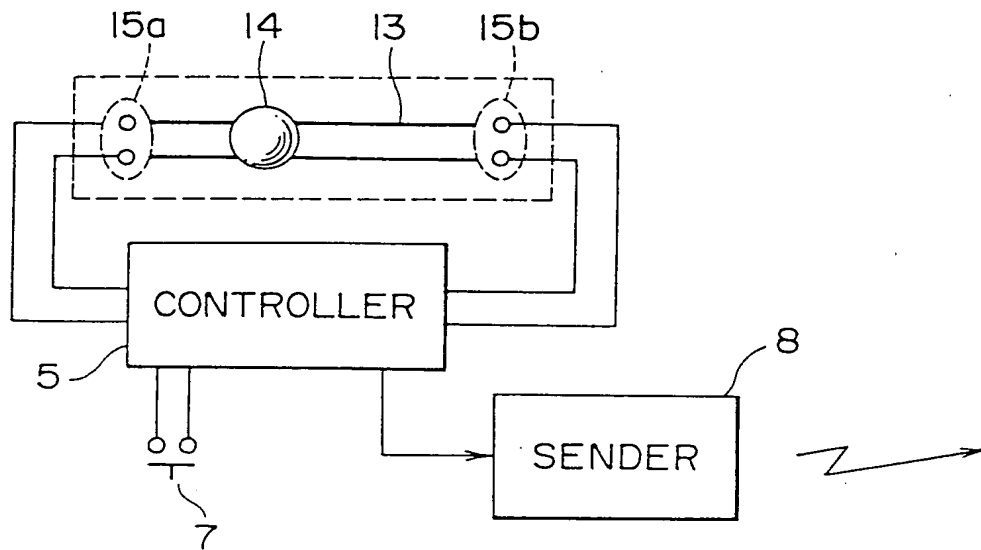


FIG. 11

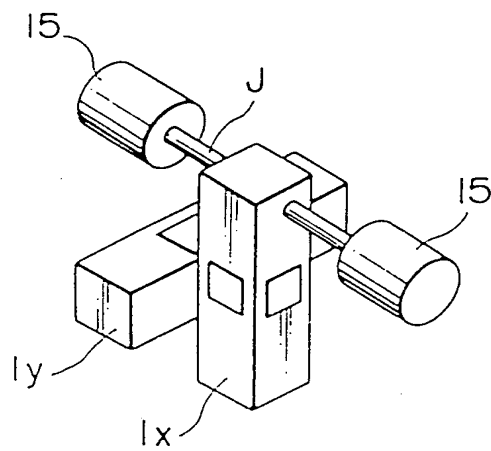


FIG. 12(a)

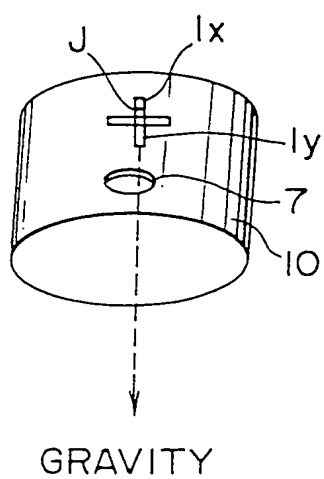


FIG. 12(b)

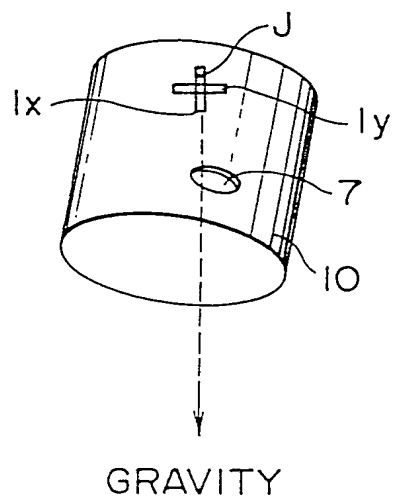


FIG. 13

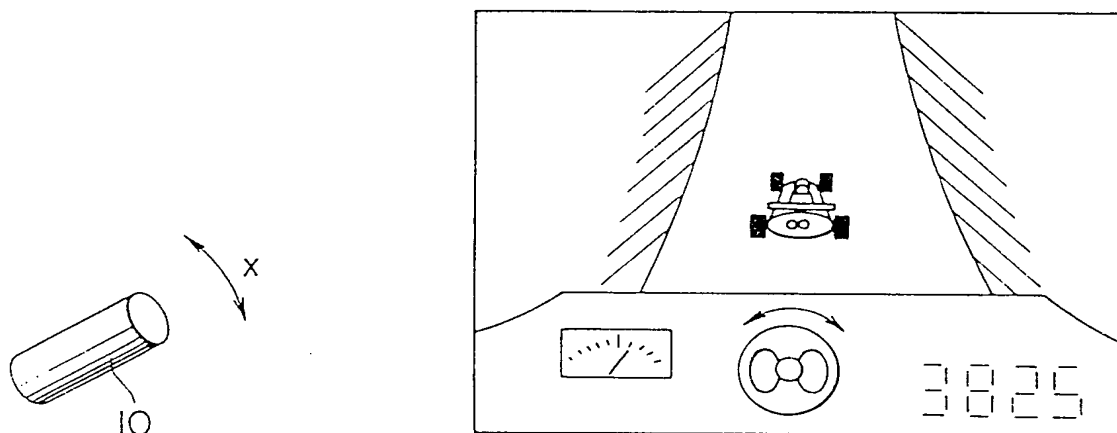


FIG. 14

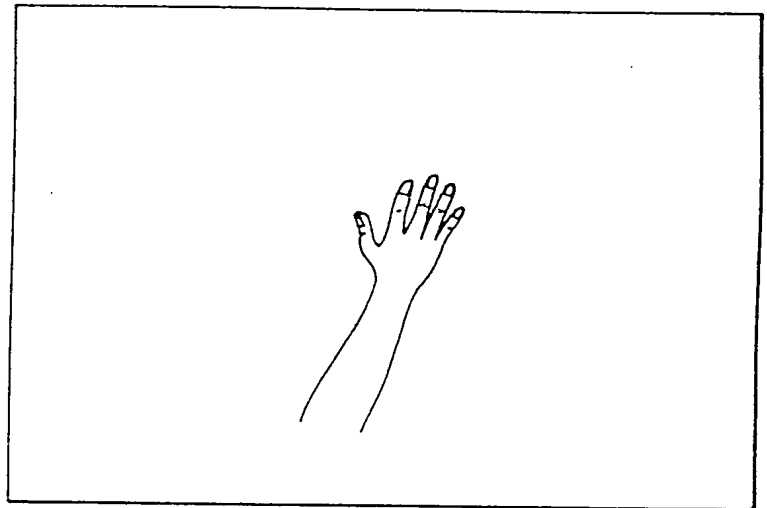
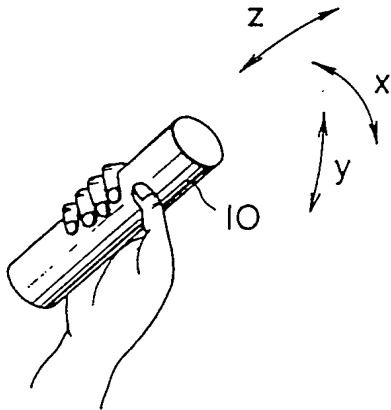


FIG. 15

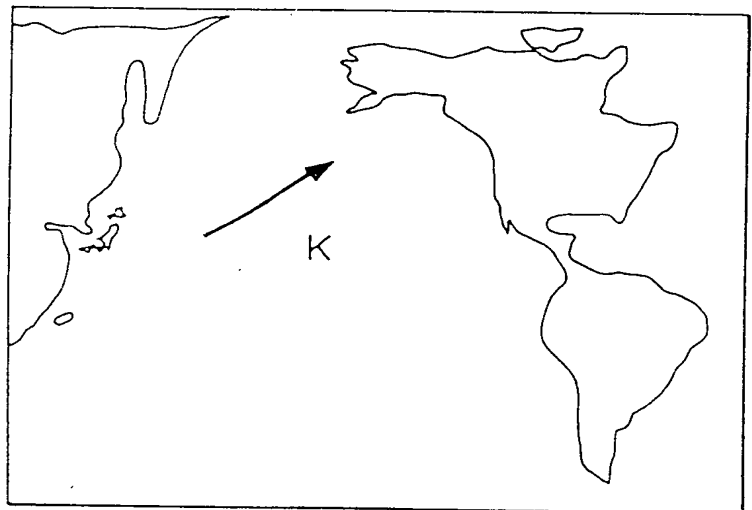
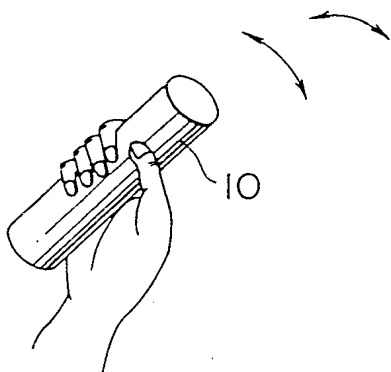


FIG. 16

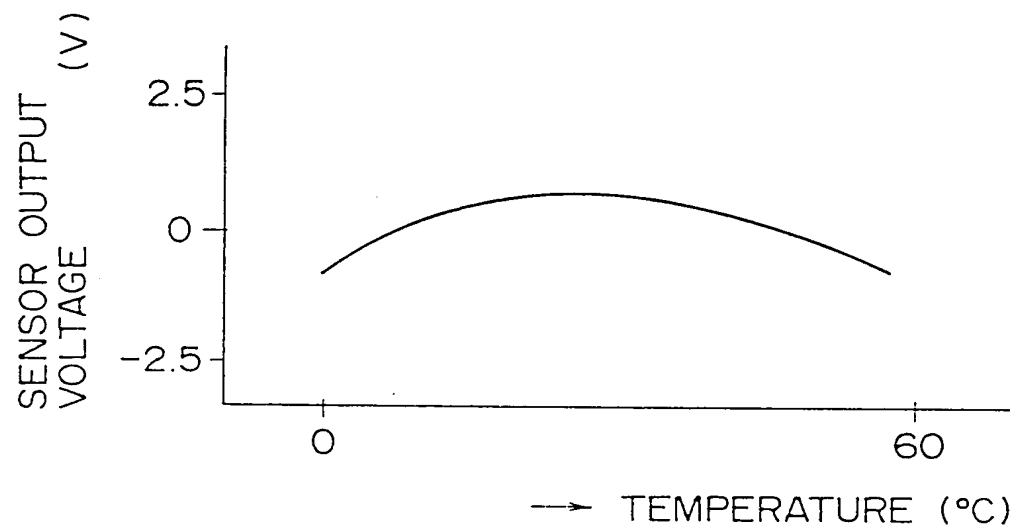


FIG. 17

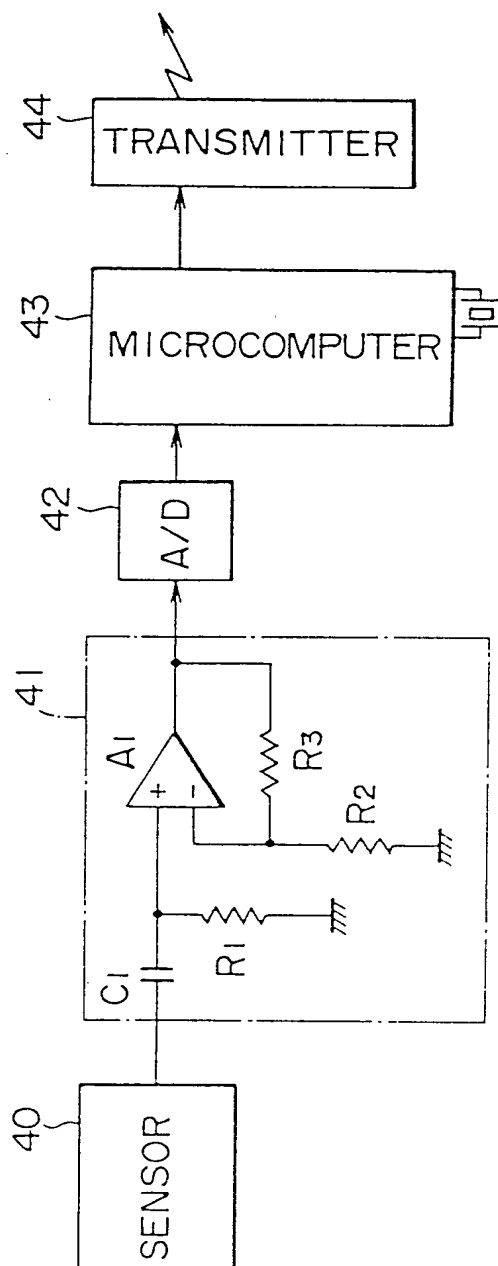


FIG. 18

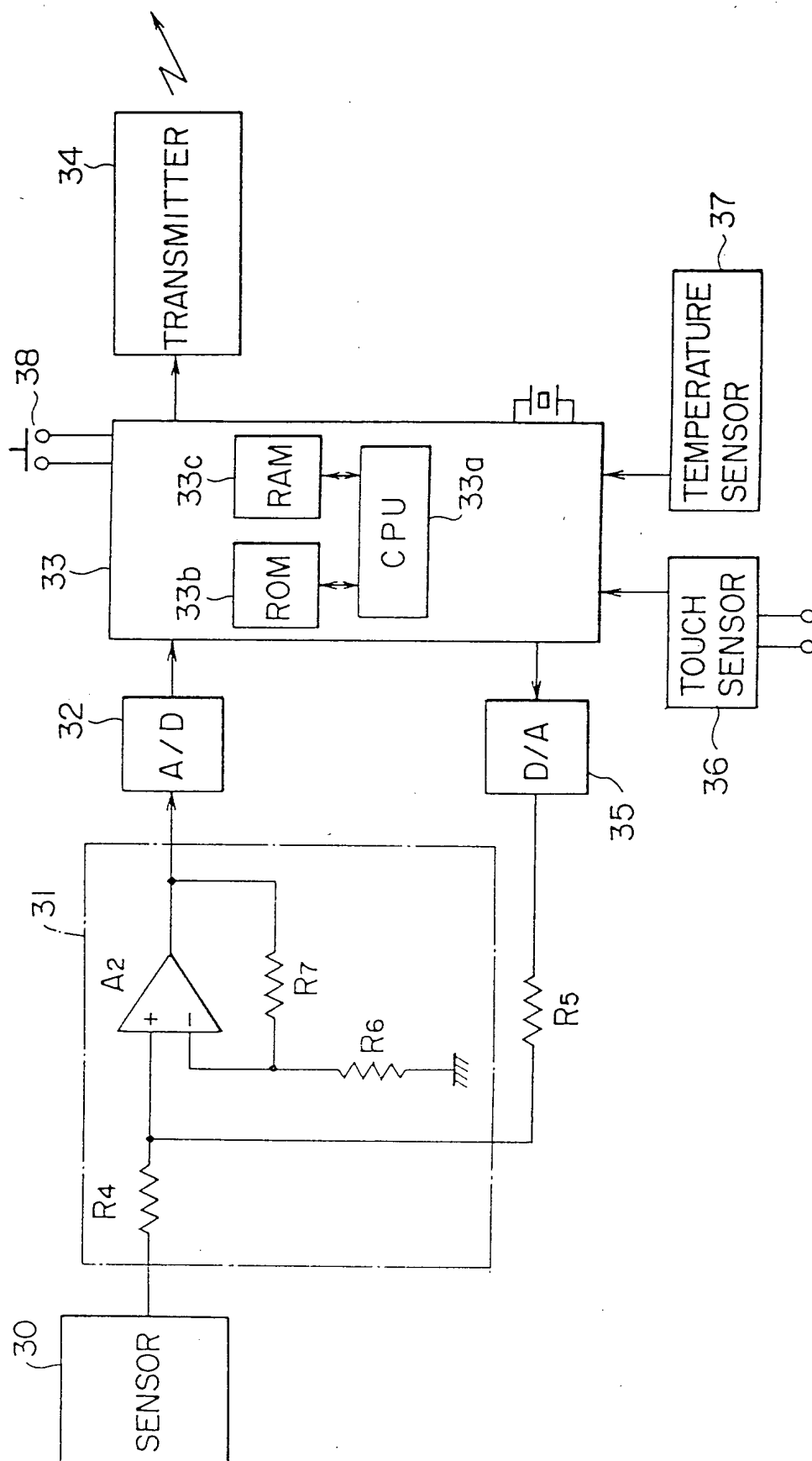
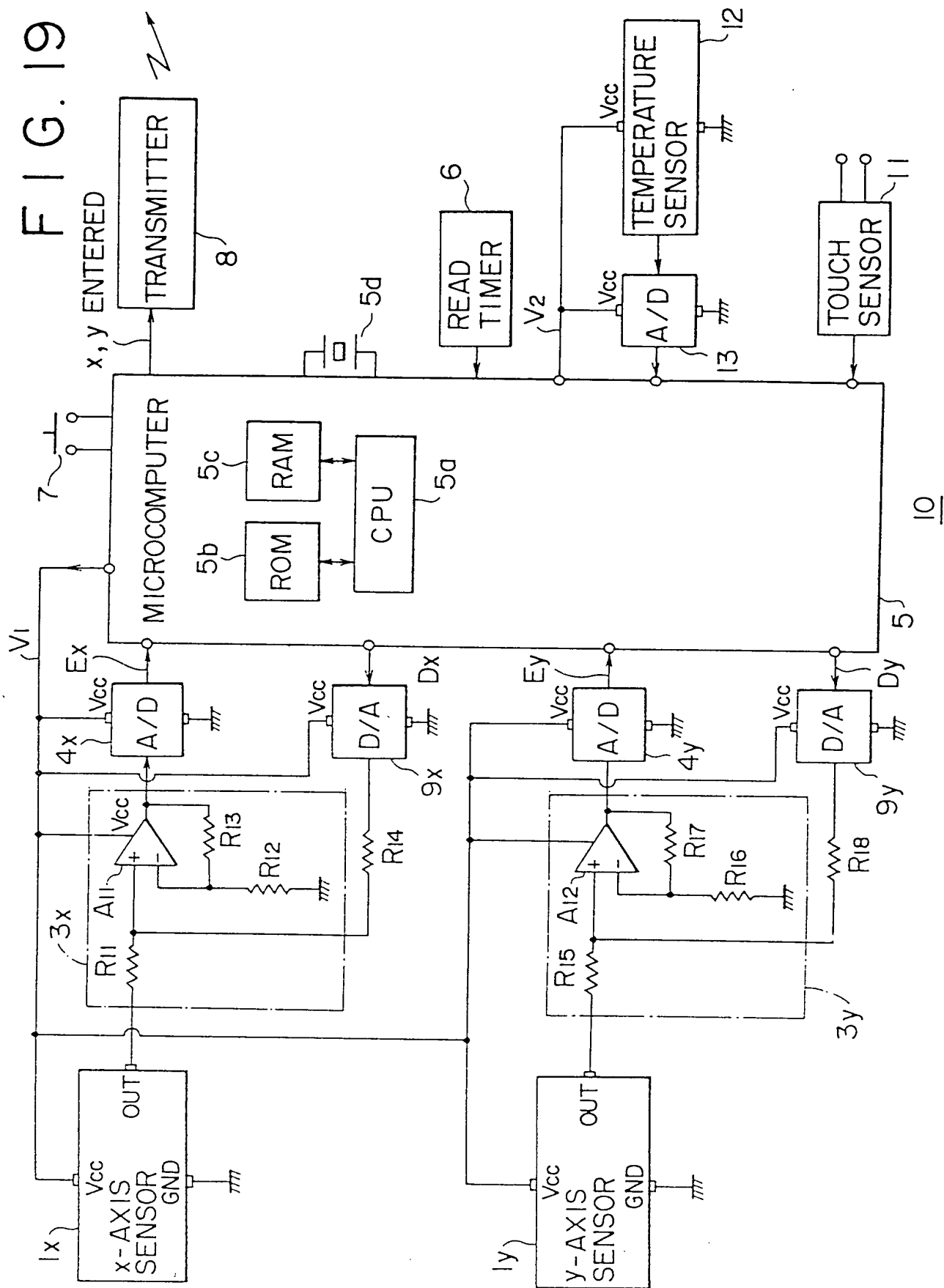


FIG. 19



F I G. 20(a)

F I G. 20(b)

F200 (TIMER INTERRUPT CAUSED)

FIG. 21A

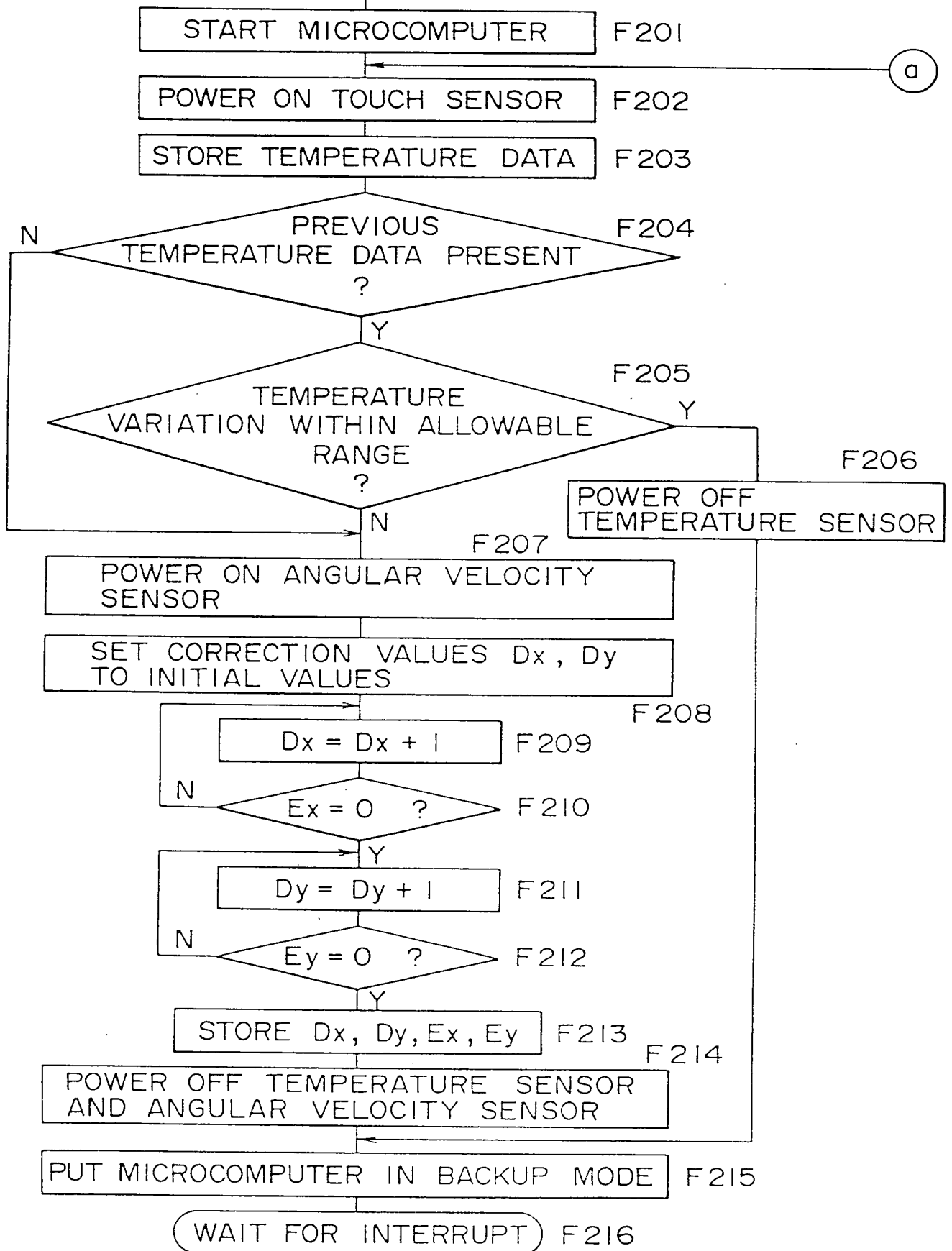


FIG. 21B

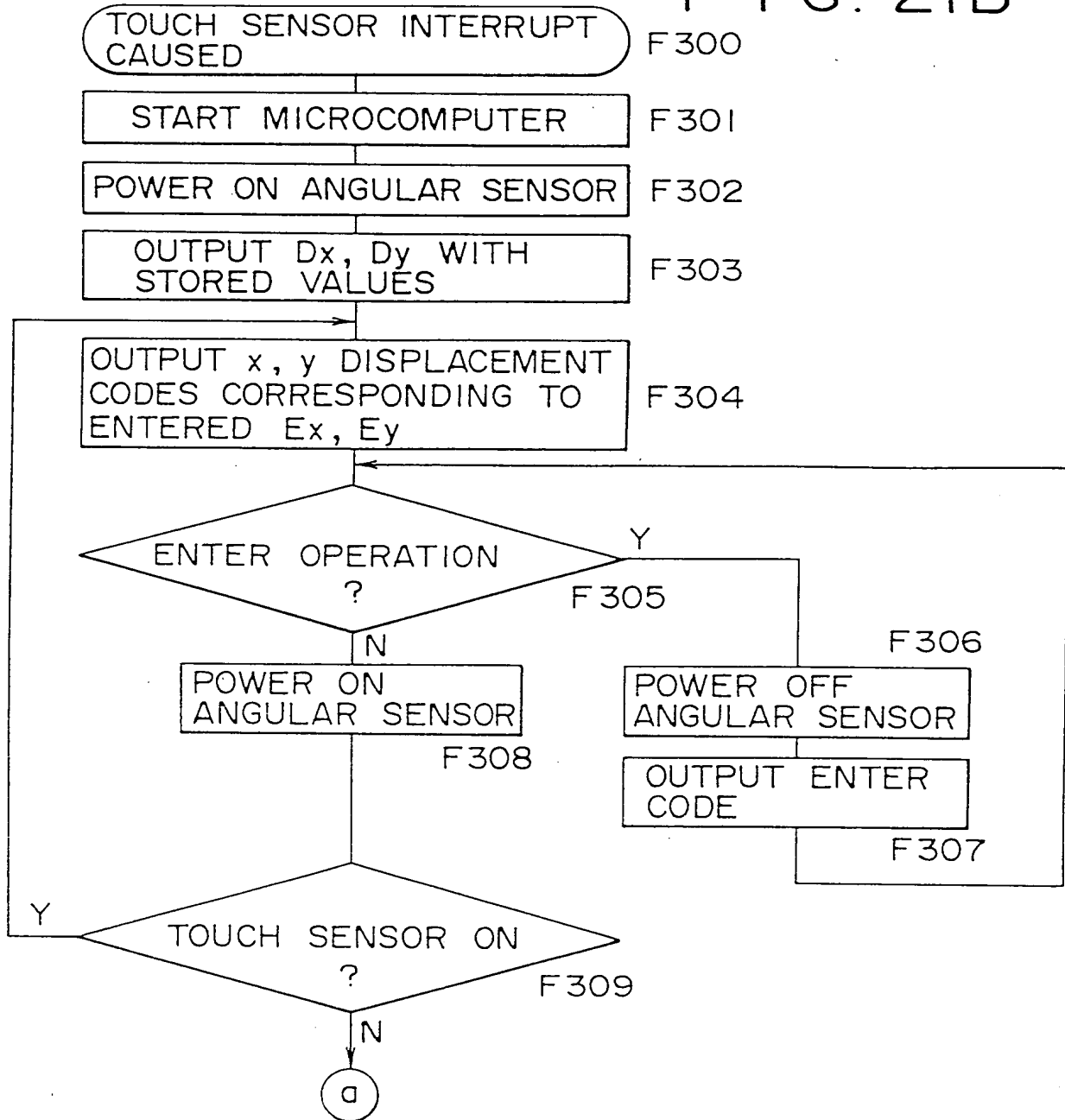


FIG. 21

